DETAILED ACTION

1. This Action is in response to Applicant's amendment filed on 11/30/2009. Claims 1, 3-8, 10-14, and 17-22 are now pending in the present application.

EXAMINER'S AMENDMENT

2. An Examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to Applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Christopher M. Swickhamer on 02/16/2010.

3. The application has been amended as follows:

IN THE CLAIMS

1. (Currently Amended) An apparatus comprising:

media data processing circuitry configured to perform at least a first data processing algorithm on media data in the apparatus; and

accessory interface circuitry configured to provide a message for transfer to a mobile telecommunications terminal, said message comprising a specification identifying the at least a first data processing algorithm performable by the media data processing circuitry included in the apparatus,

wherein the media data processing circuitry is configured, following disablement of further media data processing circuitry configured to perform at

least a second data processing algorithm on media data in the mobile telecommunications terminal, to perform the at least a first data processing algorithm on first media data in place of the performance of the at least a second data processing algorithm on the first media data in the mobile telecommunications terminal, and

wherein the at least a first data processing algorithm and the at least a second data processing algorithm are echo-canceling and/or frequency equalizing algorithms.

6. (Cancelled).

7. (Cancelled).

8. (Currently Amended) A method comprising:

providing a message for transfer from an apparatus to a mobile telecommunications terminal, the apparatus comprising media data processing circuitry configured to perform at least a first data processing algorithm on media data, the mobile telecommunications terminal comprising further media data processing circuitry configured to perform at least a second data processing algorithm on media data, and the message comprising a specification identifying the at least a first data processing algorithm performable by the media data processing circuitry; and

following disablement of the further media data processing circuitry of the mobile telecommunications terminal, performing the at least a first data processing algorithm on first media data in the apparatus in place of the performance of the at least a second data processing algorithm on the first media data in the mobile telecommunications terminal, and

wherein the at least a first data processing algorithm and the at least a second data processing algorithm are echo-canceling and/or frequency equalizing algorithms.

13. (Cancelled).

14. (Cancelled).

21. (Currently Amended) A ~~computer-readable medium~~ memory storing computer program instructions that, when executed by processing circuitry of an apparatus, cause the apparatus to perform:

providing a message for transfer from the apparatus to a mobile telecommunications terminal, the apparatus comprising media data processing circuitry configured to perform at least a first data processing algorithm on media data, the mobile telecommunications terminal comprising further media data processing circuitry configured to perform at least a second data processing algorithm on media data, and the message comprising a specification identifying

the at least a first data processing algorithm performable by the media data processing circuitry; and

following disablement of the further media data processing circuitry of the mobile telecommunications terminal, performing the at least a first data processing algorithm on first media data in the apparatus in place of the performance of the at least a second data processing algorithm on the first media data in the mobile telecommunications terminal, and

wherein the at least a first data processing algorithm and the at least a second data processing algorithm are echo-canceling and/or frequency equalizing algorithms.

22. (Currently Amended) The ~~computer-readable medium~~ memory according to claim 21, wherein the instructions further cause the apparatus to perform: transferring the first media data from the apparatus to the mobile telecommunications terminal, following the performance of the at least a first data processing algorithm on the first media data.

Allowable Subject Matter

4. Claims 1, 3-5, 8, 10-12 and 17-22 are allowed.

The following is an examiner's statement of reasons for allowance:

Consider claims 1, 8, and 21, the prior art of Pattabiraman et al. (US 20030195010 A1), Kakihara et al. (US 20030156097 A1) and Isberg et al. (US 6201975

B1), disclose an apparatus comprising: media data processing circuitry configured to perform at least a first data processing algorithm on media data in the apparatus; and accessory interface circuitry configured to provide a message for transfer to a mobile telecommunications terminal, said message comprising a specification identifying the at least a first data processing algorithm performable by the media data processing circuitry included in the apparatus.

However, after the amendments to the independent claims 1, 8, and 21, Applicant's remarks have been considered and found to be persuasive. In agreement with the Applicant's remarks, the prior arts failed to disclose wherein the media data processing circuitry is configured, following disablement of further media data processing circuitry configured to perform at least a second data processing algorithm on media data in the mobile telecommunications terminal, to perform the at least a first data processing algorithm on first media data in place of the performance of the at least a second data processing algorithm on the first media data in the mobile telecommunications terminal, and wherein the at least a first data processing algorithm and the at least a second data processing algorithm are echo-canceling and/or frequency equalizing algorithms.

Any comments considered necessary by Applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

Conclusion

5. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Marcos Batista, whose telephone number is (571) 270-5209. The Examiner can normally be reached on Monday-Thursday from 8:00am to 5:00pm.

If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's supervisor, Rafael Pérez-Gutiérrez can be reached at (571) 272-7915. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free) or 703-305-3028.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist/customer service whose telephone number is (571) 272-2600.

/Marcos Batista/
Examiner

/Rafael Pérez-Gutiérrez/
Supervisory Patent Examiner, Art Unit 2617 02/17/2010